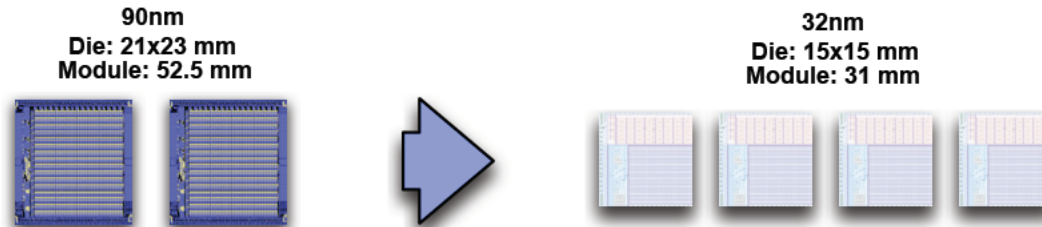


# Definition & Purpose

## From WindsorBlue to WindsorGreen

### SIMD Architecture

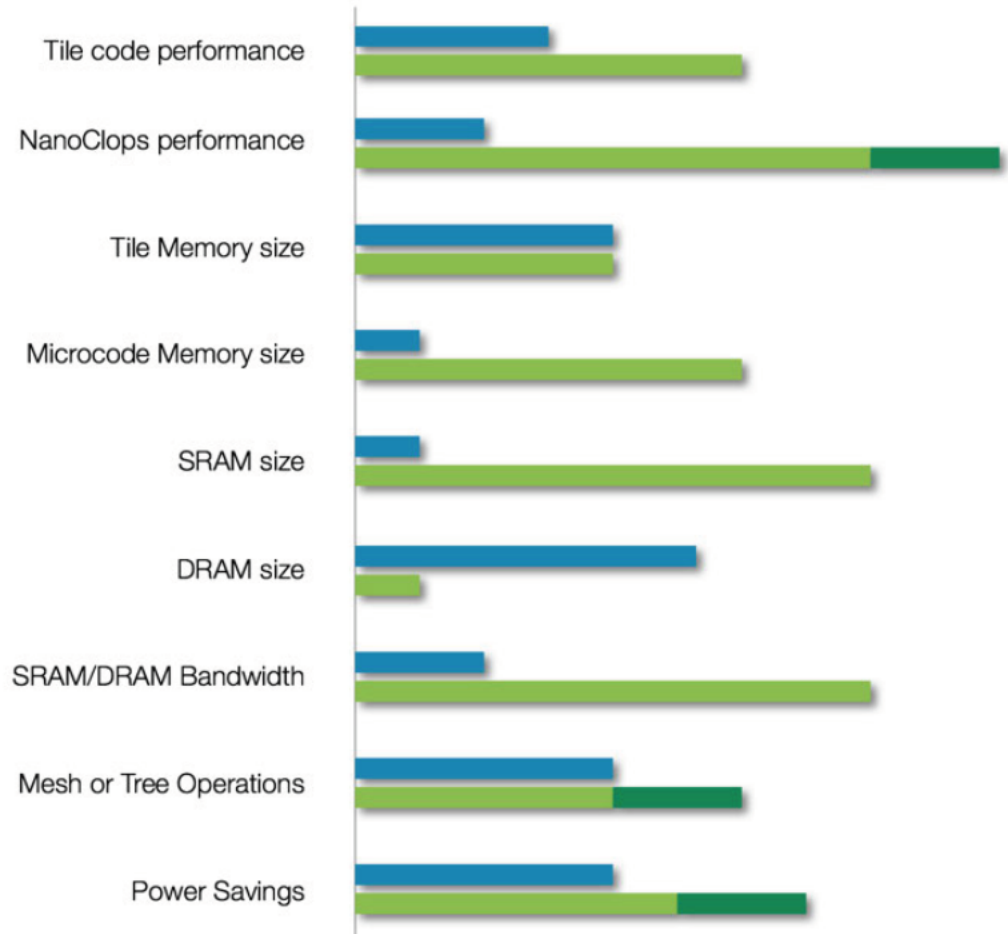


- Share same infrastructure (Cold Plate Assembly, Spine, Back Plane Assembly)
  - desire: interchangeable with old WindsorBlue cardlets
- 4 ASICs per cardlet instead of 2 for WB
- Software backward compatible (with new features added)

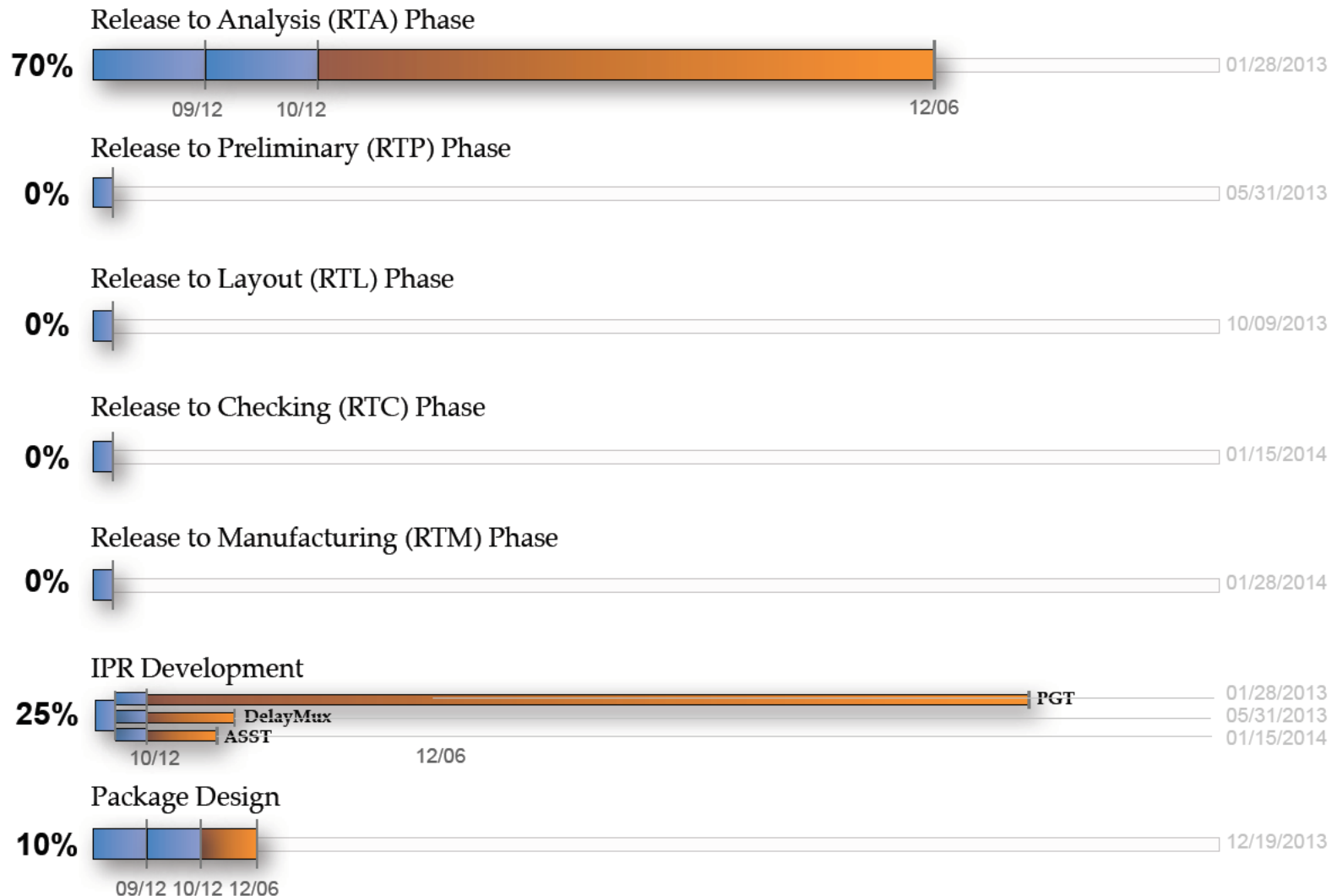
# Definition & Purpose

## WindsorGreen (New) vs WindsorBlue (OLD)

- 32nm ASIC technology
- ~20 M instances per ASIC
- 528 Processing Units per ASIC
- 56 MB embedded DRAM per ASIC
- ~128 M cores on a full system
- Partial Good Test
- Water cooled



# Progress & Outlook



# Progress & Outlook

